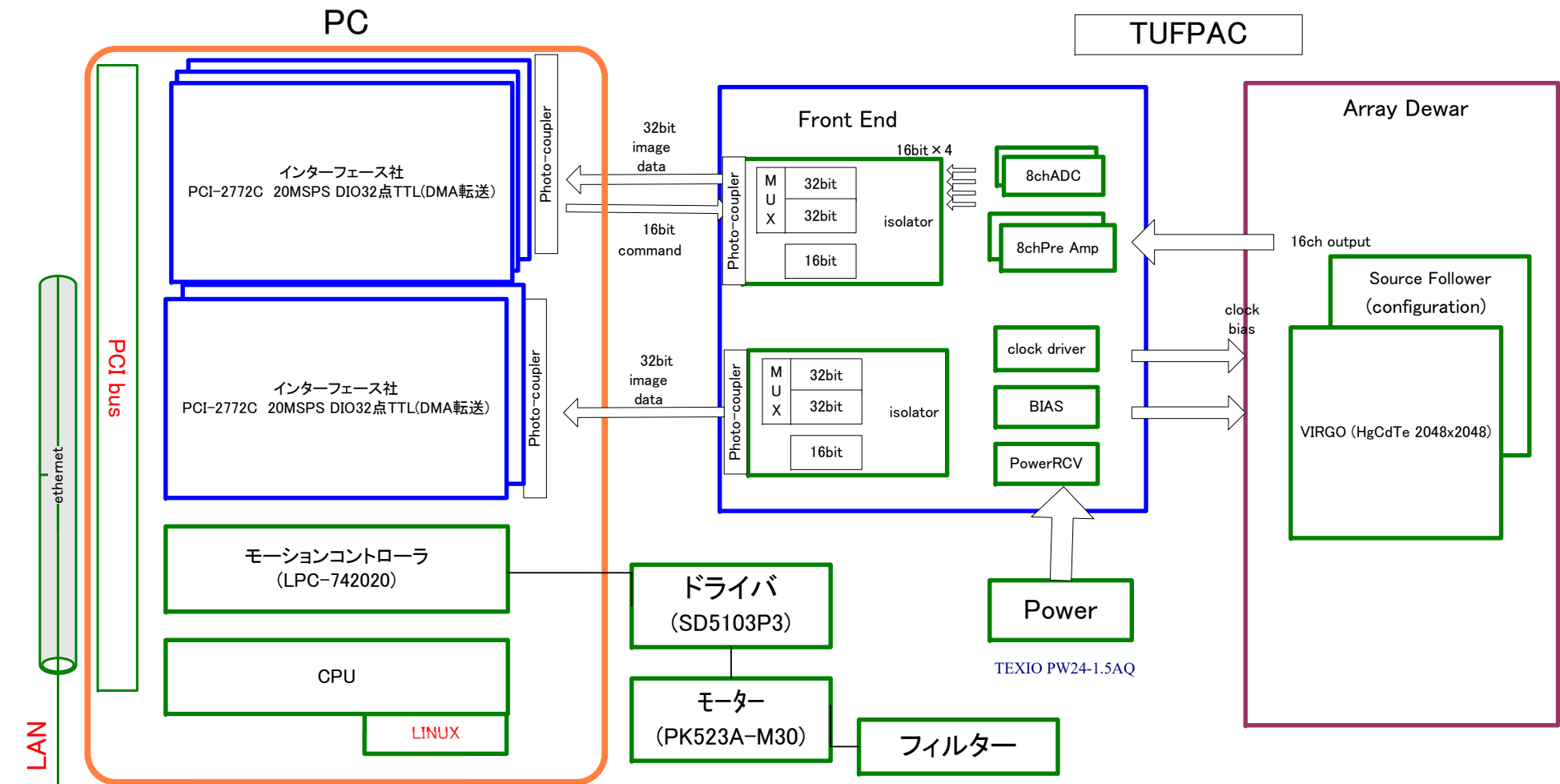


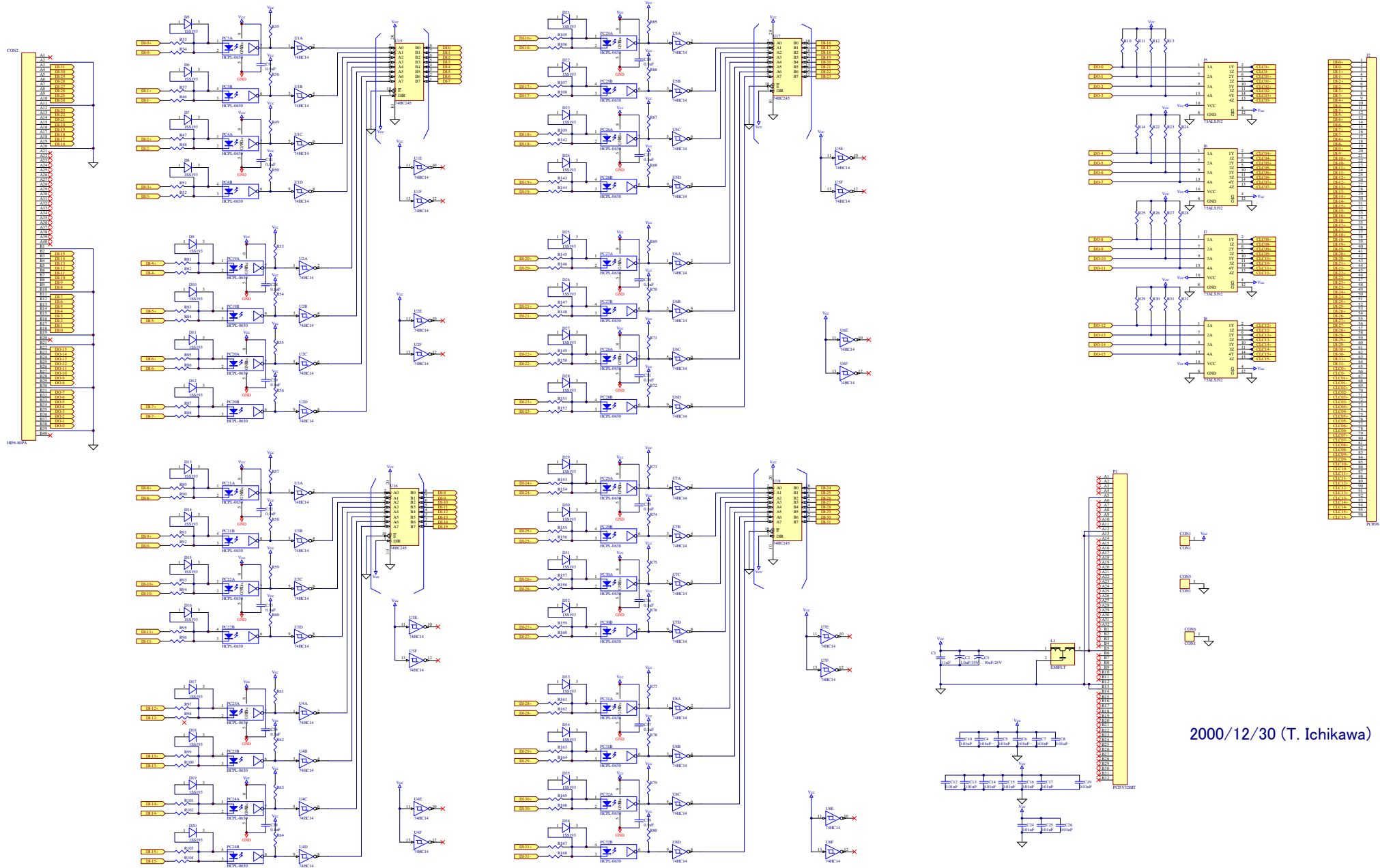
Tohoku University Array Control System (TACS)

2015/5/7 T. Ichikawa
2022/9/24 T. Ichikawa



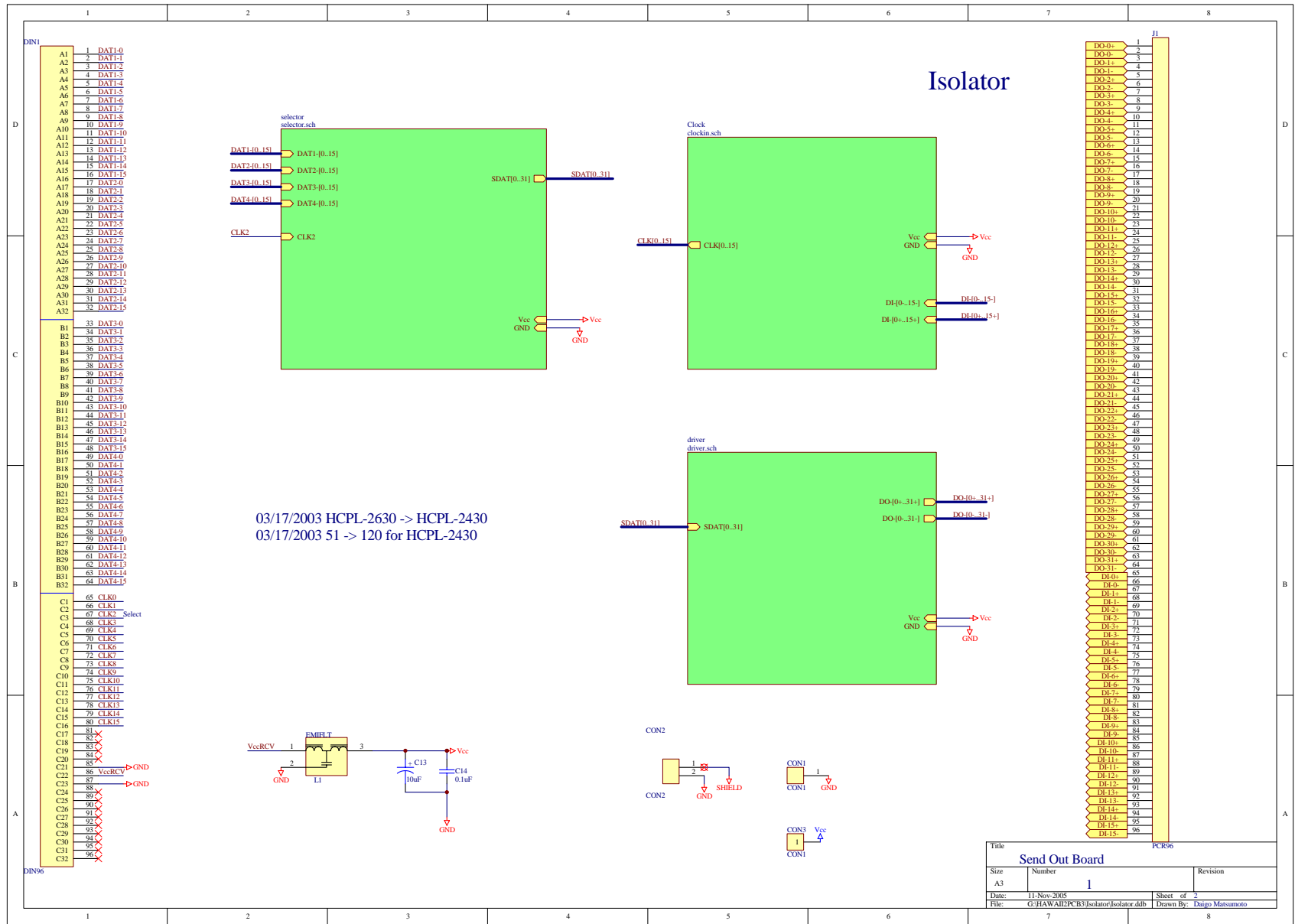
インターフェース社
PFA-C13NN19V(L6)D12
Intel Atom N2800搭載_13スロットショートサイズ
Interface Linux System (Debian系オリジナル、32ビット)

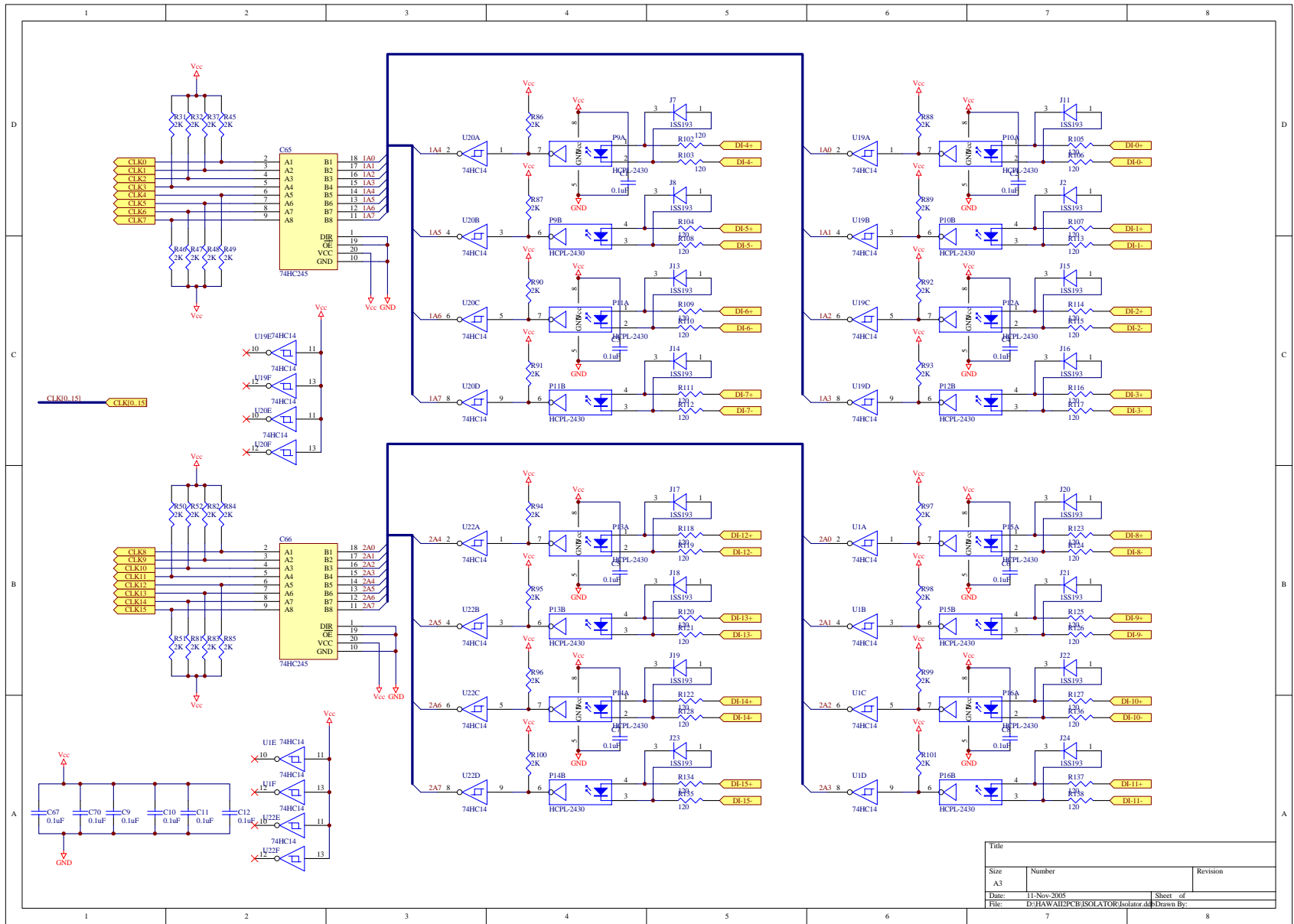
Isolation PIO for DSP board



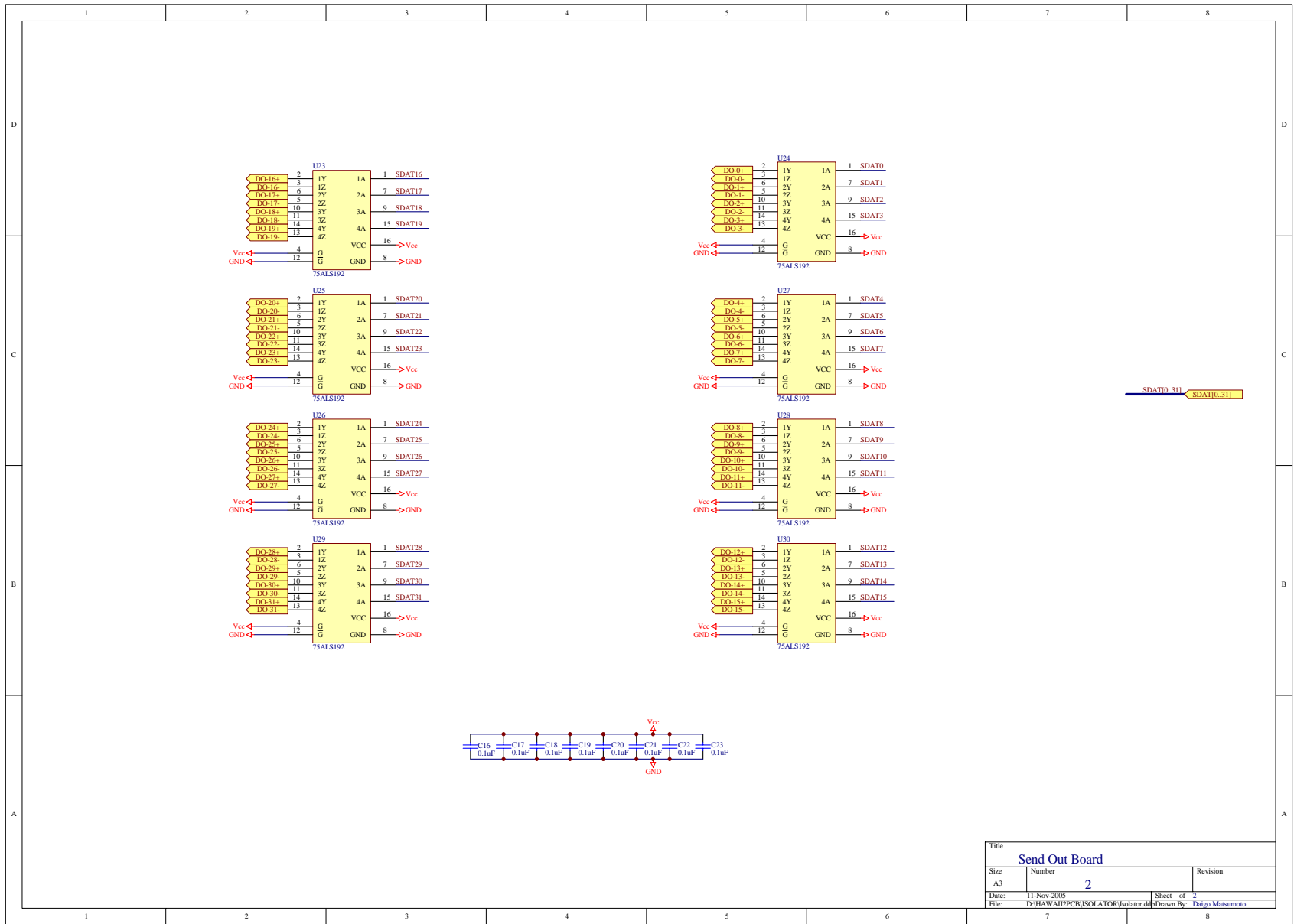
2000/12/30 (T. Ichikawa)

No.	Name	Revision
1	A1	
2		
3		
4		
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11		
12		



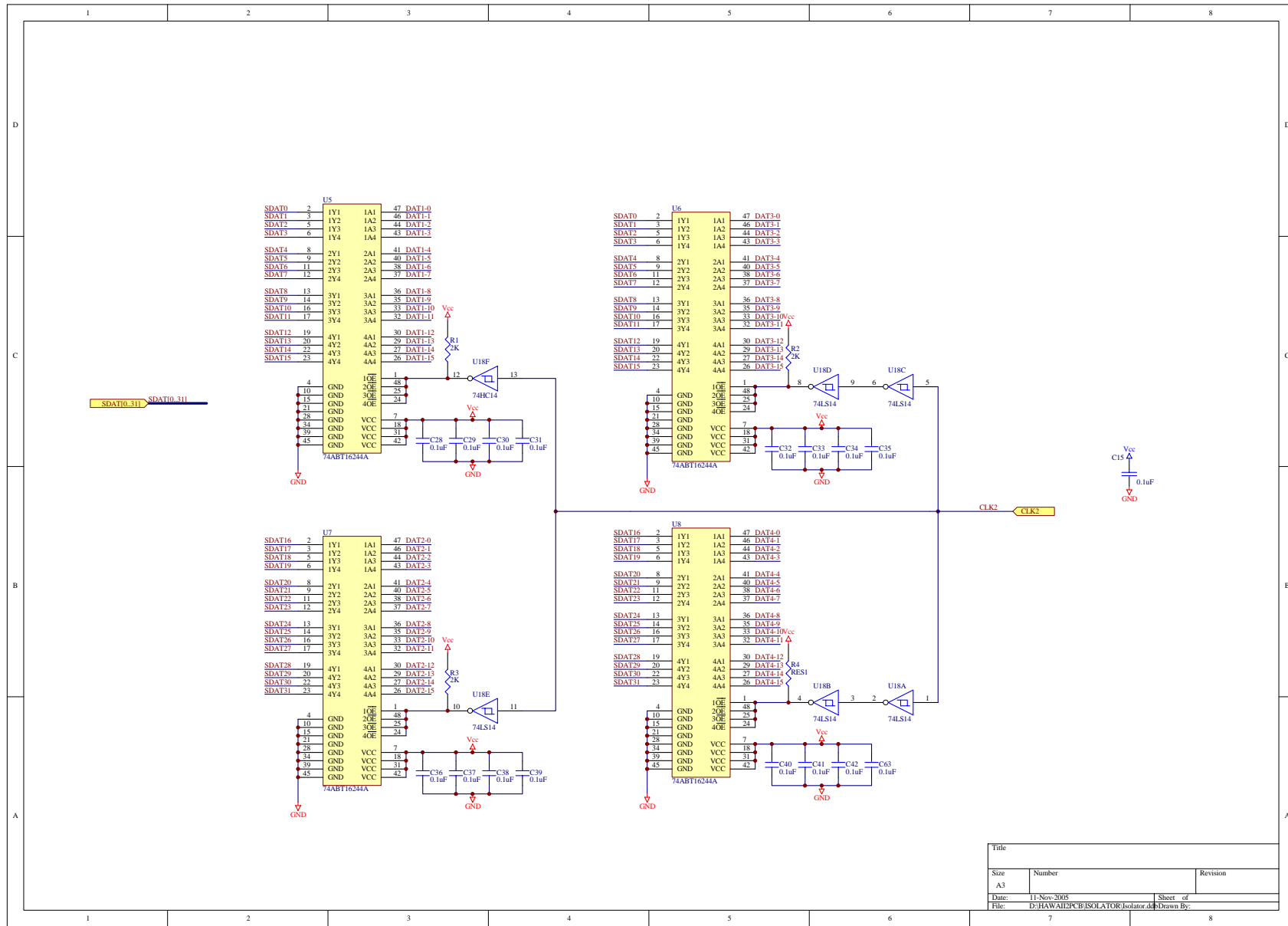


Title		
Size	Number	Revision
A3		
Date:	11-Nov-2005	Sheet of
File:	D:\HAWAII\PCB\ISOLATOR\Isolator.dwg Drawn By:	



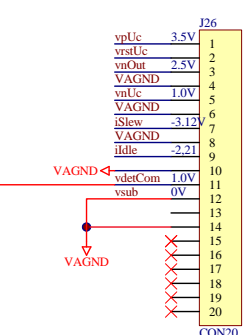
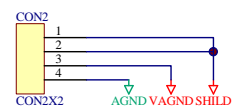
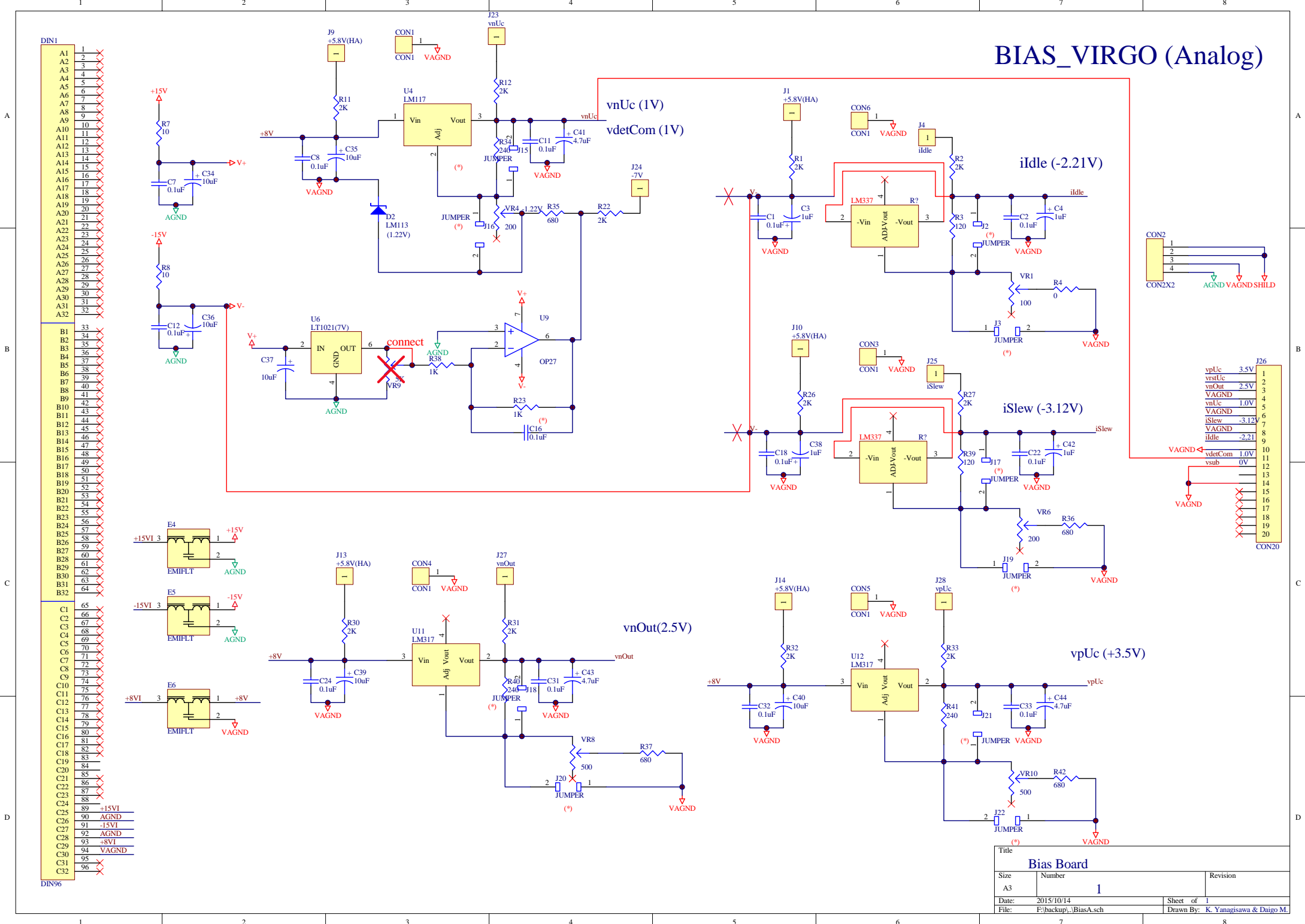
SDAT0_31 SDAT0_31

Title		
Send Out Board		
Size	Number	Revision
A3	2	
Date:	11-Nov-2005	Sheet of 2
File:	D:\HAWAII\PCB\ISOLATOR\Isolator.dwg Drawn By: Daigo Matsumoto	



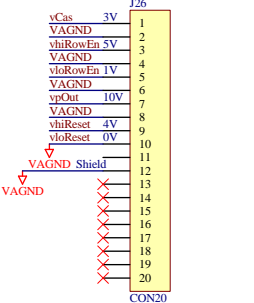
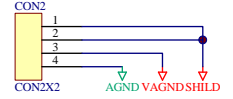
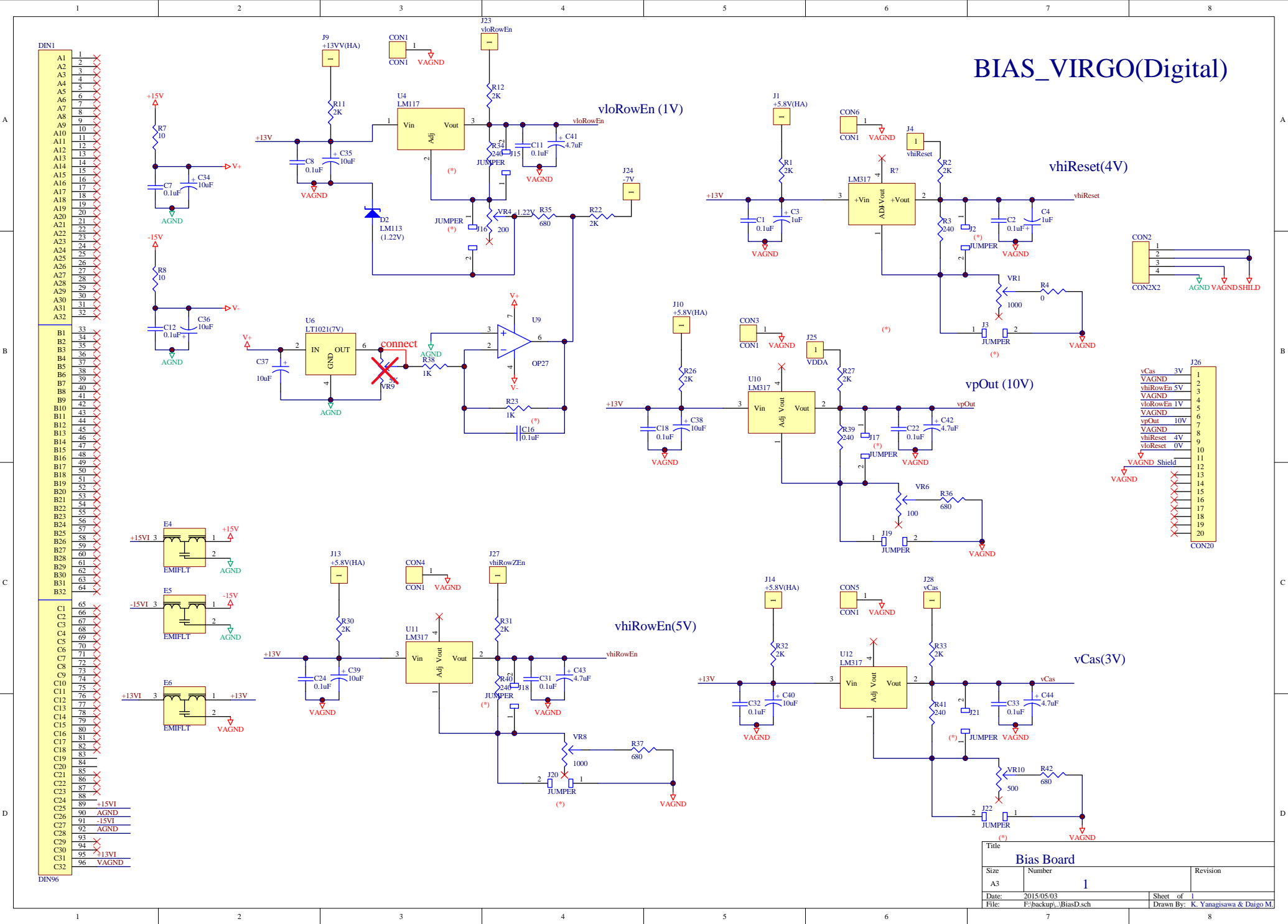
Title		
Size	Number	Revision
A3		
Date:	11-Nov-2005	Sheet of
File:	D:\HAWAII\PCB\ISOLATOR\Isolator.dwg Drawn By:	

BIAS_VIRGO (Analog)



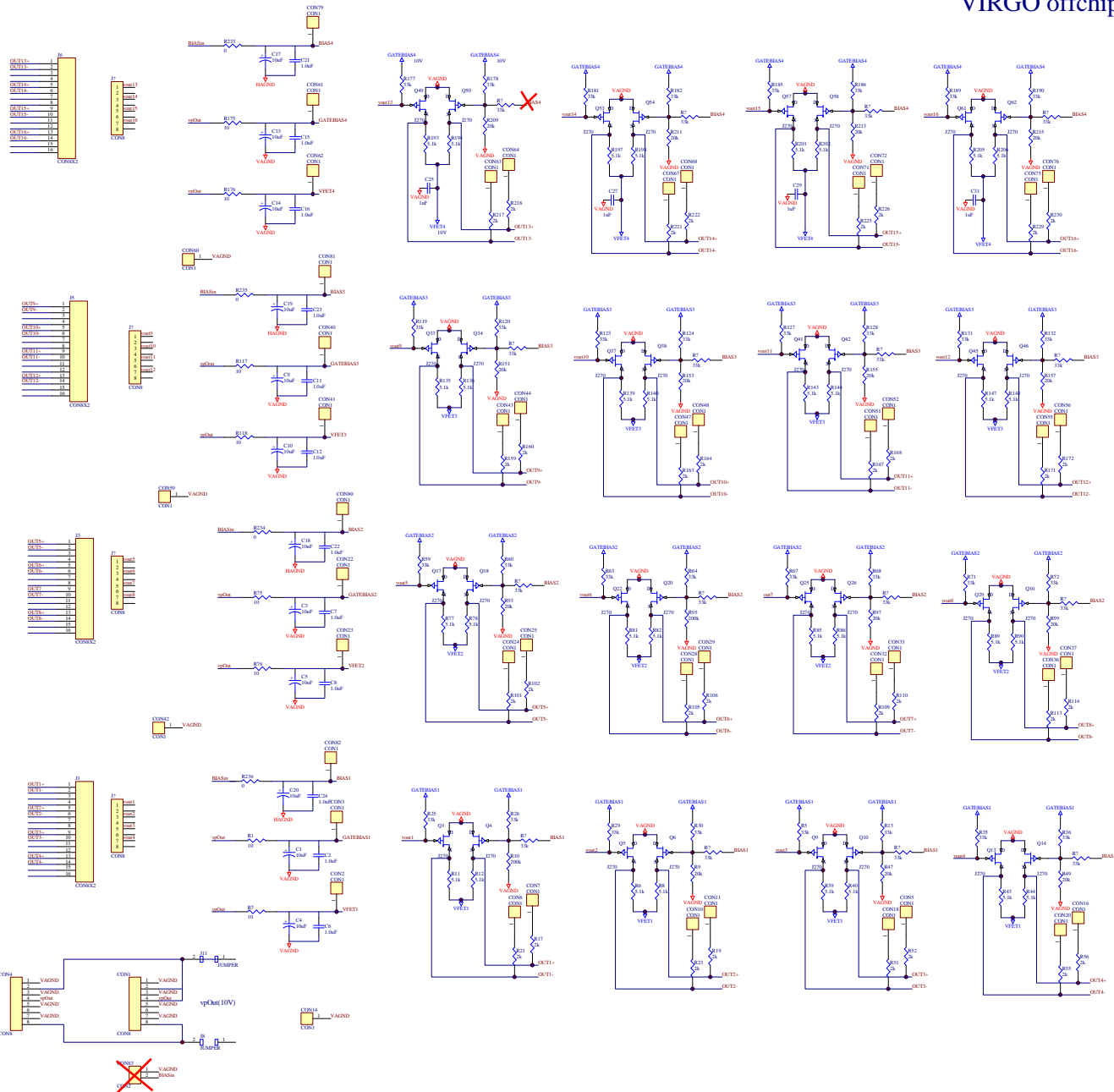
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Bias Board		
Size	Number	Revision
A3	1	
Date:	2015/10/14	Sheet of 1
File:	F:\backup\..BiasA.sch	Drawn By: K. Yanagisawa & Daigo M.

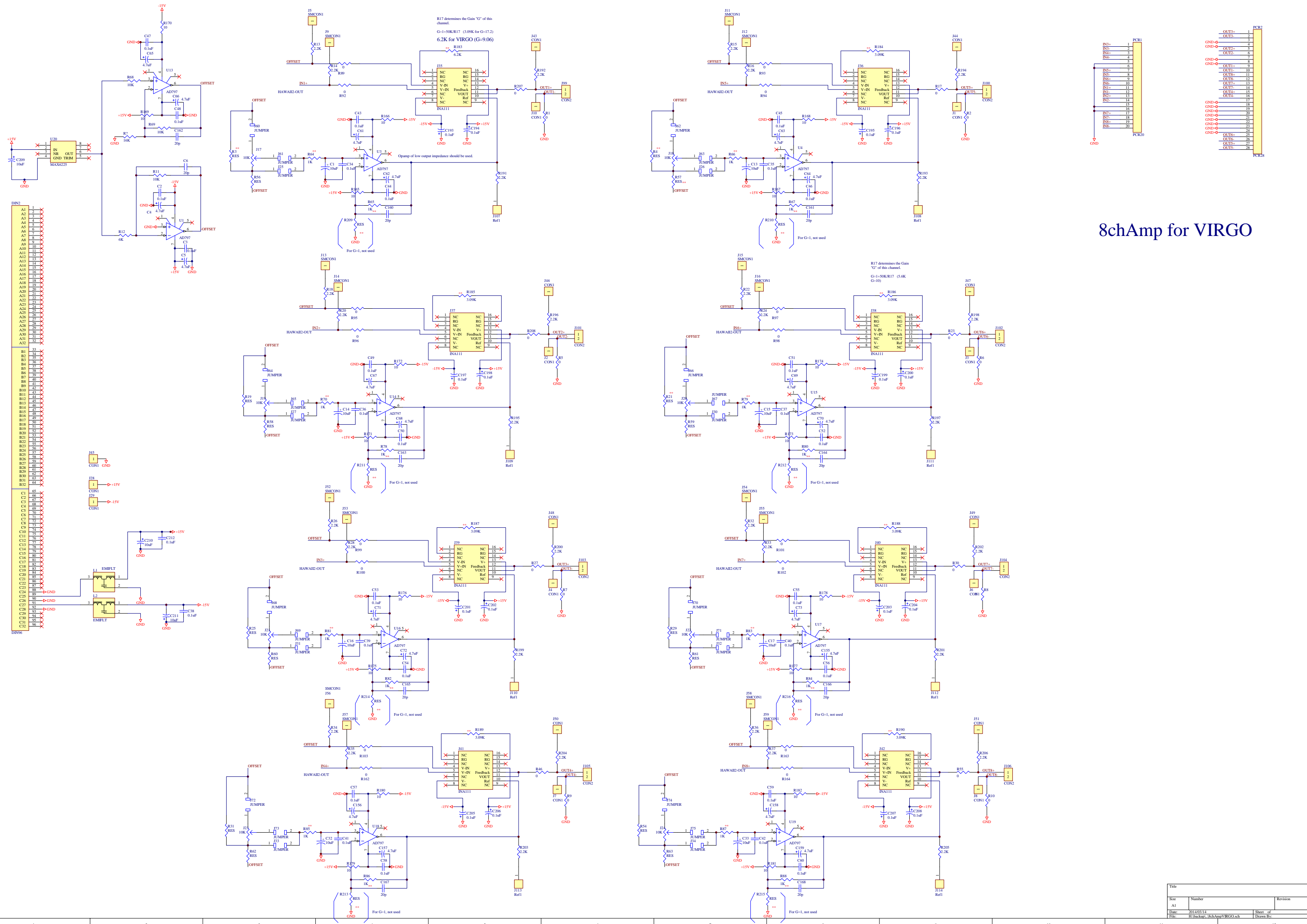
BIAS_VIRGO(Digital)



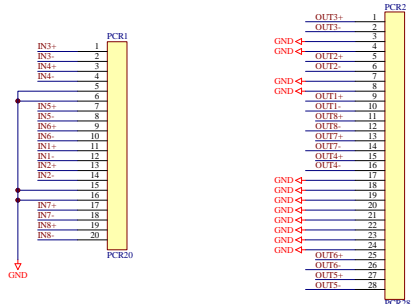
Title		
Bias Board		
Size	Number	Revision
A3	1	
Date:	2015/05/03	Sheet of 1
File:	F:\backup\...BiasD.sch	Drawn By: K. Yanagisawa & Daigo M

VIRGO offchip amp





8chAmp for VIRGO



Size	Number	Revision
A1		
Date:	2014.03.14	Sheet of
File:	H:\Backup\8chAmpVIRGO.sch	Drawn By:

ADC8ch V1

REF = 2.5V
 INA VIN REF REF
 INB VIN VIN GND GND
 Input Range 0-REF +REF +2REF 0-2REF

Power for Preamp

AD1	AD1010-1
AD2	AD1010-2
AD3	AD1010-3
AD4	AD1010-4
AD5	AD1010-5
AD6	AD1010-6
AD7	AD1010-7
AD8	AD1010-8
AD9	AD1010-9
AD10	AD1010-10
AD11	AD1010-11
AD12	AD1010-12
AD13	AD1010-13
AD14	AD1010-14
AD15	AD1010-15
AD16	AD1010-16
AD17	AD1010-17
AD18	AD1010-18
AD19	AD1010-19
AD20	AD1010-20
AD21	AD1010-21
AD22	AD1010-22
AD23	AD1010-23
AD24	AD1010-24
AD25	AD1010-25
AD26	AD1010-26
AD27	AD1010-27
AD28	AD1010-28
AD29	AD1010-29
AD30	AD1010-30
AD31	AD1010-31
AD32	AD1010-32
AD33	AD1010-33
AD34	AD1010-34
AD35	AD1010-35
AD36	AD1010-36
AD37	AD1010-37
AD38	AD1010-38
AD39	AD1010-39
AD40	AD1010-40
AD41	AD1010-41
AD42	AD1010-42
AD43	AD1010-43
AD44	AD1010-44
AD45	AD1010-45
AD46	AD1010-46
AD47	AD1010-47
AD48	AD1010-48
AD49	AD1010-49
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AD92	AD1010-92
AD93	AD1010-93
AD94	AD1010-94
AD95	AD1010-95
AD96	AD1010-96
AD97	AD1010-97
AD98	AD1010-98
AD99	AD1010-99
AD100	AD1010-100

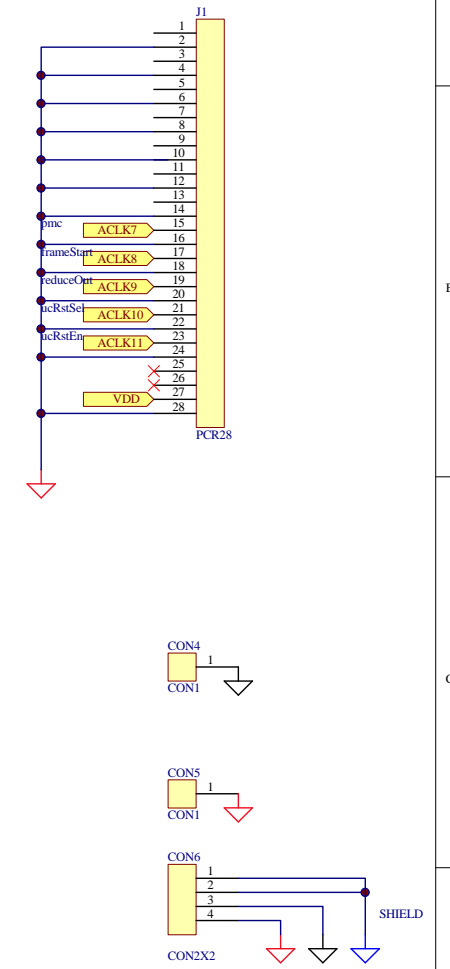
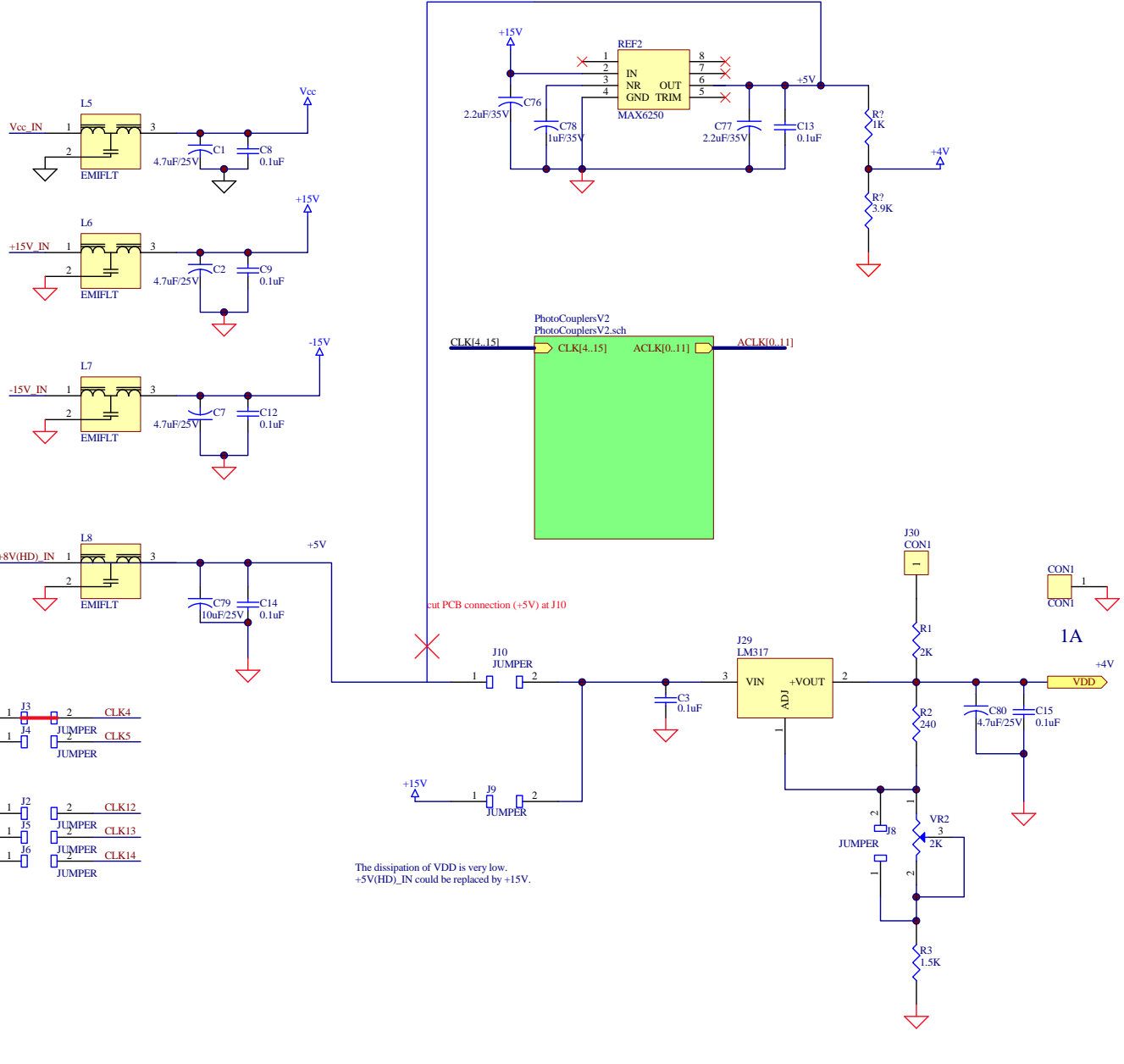
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AD97	10000000	1
AD98	10000000	1
AD99	10000000	1
AD100	10000000	1

Bug Fix

08/11/2001 two +5V -> one +5V

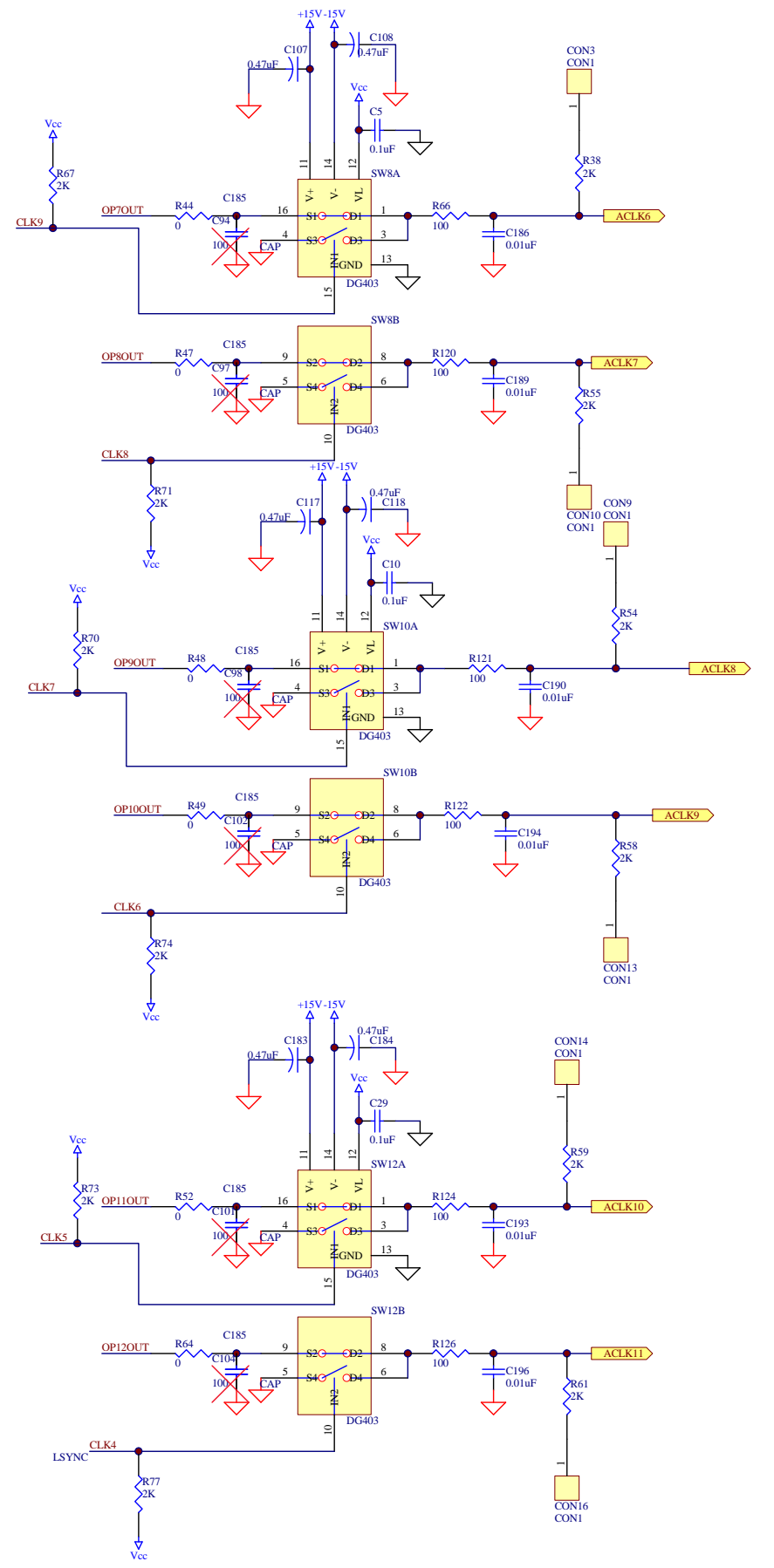
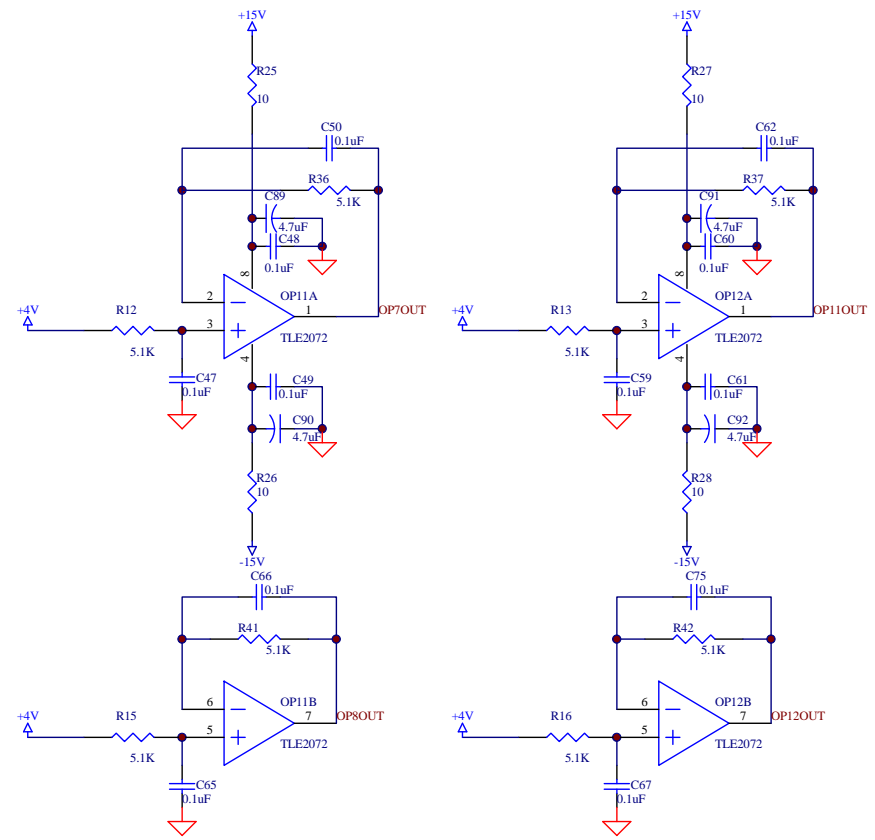
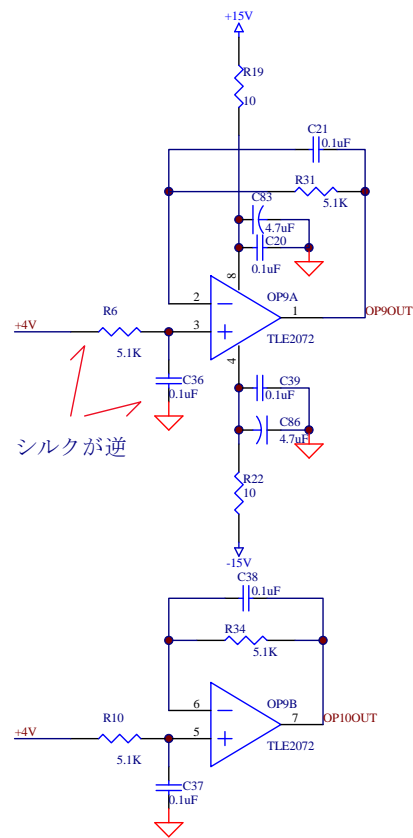
Clock Driver

- DIN2
- A1
- A2
- A3
- A4
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- A31
- A32
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- C32
- DIN96



The dissipation of VDD is very low.
+5V(HD)_IN could be replaced by +15V.

Title		
Size	Number	Revision
A3		
Date:	2015/05/03	Sheet of
File:	F:\PCB5\...\ClockDriverVIRGO.sch	Drawn By:



CLK[4..15] — CLK[4..15]

Title		
Size	Number	Revision
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Date:	2015.05.03	Sheet of
File:	F:\PCBS\PhotoCouplesVIRGO.sch	Drawn By: